



Vidyasagar University

Midnapore, West Bengal

721102



2.2.1: Additional information



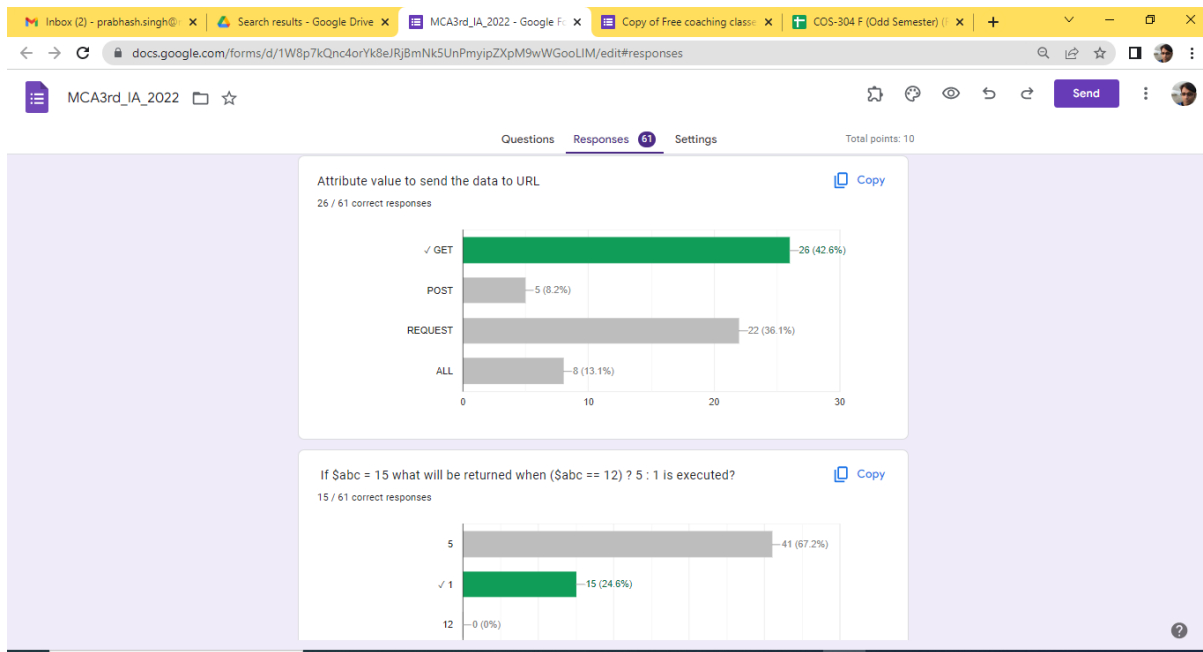
Institution assesses the learning levels



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AQAR (2022-2023)

2.2.1 - The institution assesses the learning levels of the students and organises special Programmes for advanced learners and slow learners (Sample Data and Snapshots)



Assesments through Google Forms - I



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The screenshot shows a Google Forms survey in progress. The URL in the browser is docs.google.com/forms/d/e/1FAIpQLSf09pekLbcELd2jEWIGXAlqjk2dmCRRhh9V0m3s93BraqdtZQ/formResponse. The form contains several questions with dropdown menus for answers. One dropdown menu is open, showing options 0, 1, 2, and 3. The questions are:

- 14. I was intolerant of anything that kept me from getting on with what I was doing *
- 15. I felt I was close to panic *
- 16. I was enthusiastic about anything *
- 17. I felt I wasn't worth much as a person *
- 18. I felt that I was rather touchy *

Psychometric test through online mode – Department wise



A view of Google analysis of Psychometric test



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Meet - NET/SET 4th Class by VUAA

meet.google.com/hve-wmme-bgf?pli=1&authuser=0

REC T Tapan Kumar De Faculty, Philosophy is presenting

Diparwita Mukh... and 89 more

11:22 AM

Ashrita Sahoo Suchana Hazra Sweta Pal

Sourmyajit Dutta Payal Seth Samiran Gayen

Tasrina Khan Rima Banerjee Tapan Kumar De...

NET/SET 4th Class by VUAA

Raise hand Turn on captions Tapan Kumar De Faculty, Phil... is presenting

VUAA NET/SET Class (2nd Class)

meet.google.com/xde-jyvt-eyo?authuser=0

REC T Tapan De Philosophy VU is presenting

Rachana Samanta and 93 more

11:22 PM

1. Which of the following is/are the basic paradigms in classroom teaching?

(a) Learning paradigm
(b) Instructional paradigm
(c) Both (a) and (b)
(d) None of the above

2. The prime requirement to become a good teacher is to have

(a) Genuine interest in teaching
(b) Knowledge about controlling students
(c) Subject knowledge
(d) Good expression

3. Which of the following statements is/are NOT true?

(a) Teaching is just an art
(b) Teachers can be trained only
(c) Teachers are born
(d) All of the above

4. The most desirable skill of teacher is to

(a) Make the students understand what the teacher says
(b) Cover the syllabus
(c) Give the students a chance to learn

5. Match List I with List II

List I (Level of teaching) List II (Class preparation)

A. Memory Level I. Harbart
B. Understanding Level II. Morrison
C. Reflective Level III. Hunt

Codes:
(a) A-I, B-II, C-III (b) A-I, B-III, C-II
(c) A-II, B-II, C-I (d) A-II, B-I, C-III

6. Which of the following is the sequence of different levels of teaching?

(a) Memory level-understanding level-reflective level
(b) Understanding level-memory level-reflective level
(c) Reflective level-understanding level-memory level
(d) Memory level-reflective level-understanding level

Debashila Chakraborty has left the meeting

NET/SET 2nd Class

3:26 / 1:06:40

Scroll for details

Raise hand Turn on captions Tapan De Philosophy VU is presenting

NET/SET Coaching through online mode



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5th Class of NET/SET

meet.google.com/az-nlps-bgm?pli=1&authuser=0

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P Pinaki Das Faculty, Economics is presenting Shilpa Mudi and 43 more 4:13 PM You

Data Interpretation (DI)
Mock Question set for NET/SET
Study the table given below and answer questions 1 to 5. The table shows the population of three states over the years 2002 to 2007.

State\Year	2002	2003	2004	2005	2006	2007
A	4.5	4.8	5.2	5.4	5.8	6.2
B	3.2	3.6	3.4	3.8	4.1	4.4
C	5.6	5.5	5.8	6.3	6.6	6.9

1. What is the average population of State B for all the years together (in lakhs)?
(a) 3.5 (b) 3.6 (c) 3.75 (d) 3.8

2. What is the percentage increase in population of State A between 2002 and 2003?
(a) 5 (b) 5.25 (c) 6.67 (d) 7.5

3. What was the difference between combined populations of all the three states for the years 2004 and 2005?
(a) 90,000 (b) 1,00,000 (c) 1,10,000 (d) None of the above

4. What was the average population of all the three states in 2006?
(a) 4.4 (b) 5.6 (c) 4.3 (d) 6.3

5th Class of NET/SET

12:39 / 1:25:14

Scroll for details

Raise hand Turn on captions Pinaki Das Faculty, Economics is presenting

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Advanced Learners taking help of Departmental Library



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Advanced Learners in the Research Lab



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Students participation in the Seminar



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Name - Saemi Pradhan
Roll - 03.



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34310

8/10
signature of the
invigilator with date

1. (b) Effective Access time

$$= \text{hit ratio}(\text{TLB time} + \text{Memory time}) + \text{miss ratio}(\text{TLB time} + \text{Page table time} + \text{Memory access time})$$

Here, hit ratio = 0.8

Memory access time = 100 ns

TLB access time = 20 ns

$\therefore$ Effective access time

$$= 0.80(20 + 100) + 0.20(20 + 100 + 100)$$

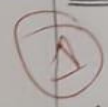
$$= (0.80 \times 120) + (0.20 \times 220)$$

$$= 96.00 + 44.00$$

$$= 140$$

$\therefore$ Effective access time = 140 ns

(a) MMU:-



MMU is used to convert a process address to physical address. MMU is used to manage memory. The full form of MMU is Memory Management Unit. When one process get a resource then it goes to waiting state. Main function is address translation.



Distinguish between Physical and Logical Address

<u>Logical Address</u>	<u>Physical Address</u>
1) Logical address is also known as virtual address.	1) Physical address is known as main memory address.
2) User can view the logical address of a program.	2) User cannot view the physical address of a program.
3) User can access logical address.	3) User cannot access physical address.
4) It is generated by CPU.	4) It is generated by MMU.

6) Logical Address space = 1024 words of 8 pages

$1024 \text{ words} = 2^{10} \text{ B} = \text{page offset}$

8 Page = 2^3 B

We know, page size = $2^{10} = \text{frame size}$

frame no. = 2^5

Diagram illustrating Logical Address (LA) and Physical Address (PA) conversion:

LA: 3 | 10 (13 bits)

PA: 5 | 10 (15 bits)

Labels: page no., frame no., page offset, frame offset

Length of Logical address = 13 bit

Length of physical address = 15 bit



3. (b) Algorithm to check a system is in safe state or not.
with length m and n
1. Let $Work$, and $Finish$ be vectors, respectively, Initially,

$Work = Available$

$Finish[i] = False$ For $i = 0, 1, 2, \dots, n-1$

2. Find i such that both
if $Finish[i] = False$
no such i is found, go to step 4

3. $Work = Work + Allocation$
 $Finish[i] = True$
go to step 2

4. If $Finish[i] == True$ for all i , then
the ~~state~~ ^{system} is in safe state.

(a) compare among safe, unsafe and deadlock states.

Safe state:-

If all process get resources at a time, then it is called safe state

Unsafe state:-



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Name- Sudipta Gayen Roll-08, Paper- COS 301
34322
Sub- Advanced operating system
9/10
19.12.2022
signature of the invigilator with date

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a) MMU—
It is known as memory management unit.
physical address ~~are~~ ^{are} generated by MMU.
MMU is the hardware component that is responsible for mapping the logical address to physical address.
CPU always generate logical address. MMU generate physical address. physical address helps to find the location of frame where the required page is store.
Give Diagram of MMU

b) TLB Hit ratio = 0.8
TLB Miss " = $1 - 0.8$
= 0.2
RAM access time = 100ns
TLB access time = 20ns
First write the formulae and then put the value
$$\therefore \text{Effective access time} = 0.8(20 + 100) + 0.2(20 + 2 \times 100)$$
$$= 0.8 \times 120 + 0.2 \times 220$$
$$= 96 + 44$$
$$= 140 \text{ ns}$$